## **REMARKS**

This Preliminary Amendment is submitted in advance of the first examination of the subject application in order to correct typographical errors and insert the serial numbers and filing dates of the related applications. No new matter has been entered.

Respectfully submitted,

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## **VERSION WITH MARKINGS TO SHOW CHANGES MADE**

## **RELATED APPLICATIONS**

This application contains material related to the following commonly assigned U.S. Patent Applications, which were concurrently filed with this application, and are hereby incorporated by reference:

Serial No. <u>09/745,655, [] filed 22 December 2000</u>			
[] for "SYSTEM AND METHOD FOR PROGRAMMING THE VALUE			
OF FRAME SYNCHRONIZATION WORDS IN A MULTIDIMENSIONAL			
DIGITAL FRAME STRUCTURE"			
Serial No. <u>09/747,380, [] filed 22 December 2000</u>			
[] for "SYSTEM AND METHOD FOR PROGRAMMING THE BIT			
ERROR RATE OF FRAME SYNCHRONIZATION WORDS IN A			
MULTIDIMENSIONAL DIGITAL FRAME STRUCTURE"			
Serial No. <u>09/746,152, [] filed 22 December 2000</u>			
[] for "SYSTEM AND METHOD FOR PROGRAMMING THE			
QUANTITY OF FRAME SYNCHRONIZATION WORDS IN A			
MULTIDIMENSIONAL DIGITAL FRAME STRUCTURE"			
Serial No. <u>09/745,793, [] filed 22 December 2000</u>			
[] for "SYSTEM AND METHOD FOR SELECTIVELY			
BROADCASTING A MULTIDIMENSIONAL DIGITAL FRAME STRUCTURE"			
Serial No. <u>09/747,072,[]</u> filed <u>22 December 2000</u>			
[] for "SYSTEM AND METHOD FOR PROGRAMMING LOSS OF			
SYNCHRONIZATION IN A MULTIDIMENSIONAL DIGITAL FRAME			
STRUCTURE"			

	Serial No. <u>09/745,774, [</u>	_] filed <u>22 December 2000</u>	
	] for "SYSTEM AND METHOD F	OR PROGRAMMING	
SYNCHRONIZATION CRITERIA IN A MULTIDIMENSIONAL DIGITAL FRAME			
STRUCTURE"			
	Serial No. <u>09/746,159,[</u>	] filed <u>22 December 2000</u>	
	] for "SYSTEM AND METHOD F	OR TRANSLATING OVERHEAD	
BYTES IN A MULTIDIMENSIONAL DIGITAL FRAME STRUCTURE"			
	Serial No. 09/527,343, filed 17 Marc	ch 2000 for "TRANSPOSABLE	
FRAME SYNCHRONIZATION STRUCTURE"			
	Serial No. 09/528,021, filed 17 Marc	ch 2000 for "PROGRAMMABLE	
SYNCHRON	IIZATION STRUCTURE WITH AU	XILIARY DATA LINK"	